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<b>Date:</b> March 21, 2006	<b>Pages:</b> 14 (including this sheet)
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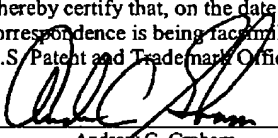
<b>Title:</b>	INTEGRATED CIRCUIT INCORPORATING THREE-DIMENSIONAL MEMORY ARRAY WITH DUAL OPPOSING DECODER ARRANGEMENT		
<b>Application No.:</b>	10/774,818	<b>Filed:</b>	February 9, 2004
<b>Examiner:</b>	Thong Q. Le	<b>Group Art Unit:</b>	2827
<b>Atty. Docket No.:</b>	023-0025	<b>Confirmation No.:</b>	9232

**ATTACHED HERETO:**

- (1) Corrected Amendment (13 pages)

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**RECEIVED  
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Applicant(s): Mark G. Johnson, Thomas H. Lee &amp; James M. Cleeves

Title: INTEGRATED CIRCUIT INCORPORATING THREE-DIMENSIONAL  
MEMORY ARRAY WITH DUAL OPPOSING DECODER  
ARRANGEMENT

Application No.: 10/774,818

Filed: February 9, 2004

Examiner: Thong Q. Le

Group Art Unit: 2827

Atty. Docket No.: 023-0025

Confirmation No.: 9232

March 21, 2006

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**CORRECTED AMENDMENT**

This paper is responsive to the correspondence mailed March 16, 2006, which states that the amendment filed on March 8, 2006 is non-compliant. Submitted hereby is a corrected section of the non-compliant amendment in compliance with 37 C.F.R. § 1.121, the corrected section being the *Amendment to the Claims*. In the complete listing of the claims, the duplicate claim 10 (labeled "original") shown in the previous response is removed.

While not requested in the March 16 correspondence, a corrected *Remarks* section is also submitted hereby. The only correction to that previously filed is found on page 12, in the listing of claim 13. The word "wordlines" in the second-to-last line of the claim is now underlined.